



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil K. Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837 Filed: March 31, 2004

Examiner: Rajnikant B. Paten Group Art Unit: 2838

Atty. Docket No.: 026-0044

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

REQUEST FOR STATUS OF APPLICATION

Dear Sir:

Applicant(s) hereby request a report on the status of the above-identified patent application. If a telephone call would expedite this request, please call the undersigned at the number shown below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Nicole Teitler Cave".

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